

AMENDMENTS TO THE ABSTRACT

Please amend the abstract as follows:

Pulse-generator circuit for generating an input signal for a flip-flop circuit from a clock-pulse signal and a data signal. The circuit includes a control unit for controlling a clock-pulse field effect transistor, a logic field effect transistor and a feedback field effect transistor. To generate the input signal, the control unit is configured in such a way that the clock-pulse field effect transistor is controlled chronologically after the logic field effect transistor and the feedback field effect transistor, thus generating the flip-flop signal.